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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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Naoto Abe

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EXAMINER

SHENG, TOM V

ART UNIT

PAPER NUMBER

2629

DATE MAILED: 07/14/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/775,168	Applicant(s) ABE ET AL.	
	Examiner Tom V. Sheng	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 May 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 43-52 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 43-52 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>1/12/06 & 6/16/05</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 43, 45, 48 and 50 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee et al. (US RE38108 E), hereinafter Chee, in view of Takasu (US 6,523,127 B1).

As for apparatus claim 43 and associated method claim 48, Chee teaches a display apparatus (notebook 10; fig. 1) comprising:

a display panel (LCD 12; column 3, lines 66-67) including a plurality of display elements (inherent liquid crystal elements);

display control means (video display controller 30; fig. 2 and 3; column 4, line 20 through column 5, line 50) for controlling said display panel in a normal display mode (inherent), a first power saving mode (PSM 1, as defined in register 60; fig. 4; column 5, lines 51-67), a second power saving mode (PSM 2 or PSM 3) and a third power saving mode (PSM 4); and

mode transition means (power saving controller 58, PLL timers 66, 68, pin PS1 and PS2, of the video display controller 30; fig. 3 and 6) for causing said display panel to shift to the first power saving mode or the third power saving mode from a normal

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mode based on an instruction of a user (for example, closing the notebook cover or engaging a shutdown switch engages PSM 1 or PSM 4; column 6, lines 15-34), and causing said display panel to shift to the second power saving mode from the normal mode based on status of said display panel (when inactivity is determined over a selected time interval, mode PSM 2 or PSM 3 would be entered; column 6, lines 1-14 and 35-52).

Further, Chee further teaches that the power saving features of each mode is configurable. Specifically as applicable to LCD, power saving features such as memory clock on/off (Mclk), pixel clock on/off (Pclk), LCD backlight on/off (LCDbl), LCD on/off (Display off), slower pixel clock (Pclk slow), and reduced gray scale (REDUCE GS) are programmable with respect to each of the power saving modes. See column 7 line 11 through column 8 line 4. Thus, for example, the first power saving mode (PSM 1) can have a slow PCLK, the third power saving mode (PSM 4) can have a reduced gray scale, and the second power saving mode (PSM 2 or 3) can have the LCD backlight turned off.

Chee does not specifically teach wherein the first power saving mode is a mode in which said display panel displays an image, since Chee's first power saving mode is triggered when the user closes the notebook cover or engages a shutdown switch, either which would shut off power and not meant for continuous display of an image.

Takasu teaches a display device having a power saving mode. Specifically, Takasu teaches a display device with a power saving button 15 (fig. 1) allowing an user

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to operate, resulting in either power off of the display device (i.e. no display) or a reduction of the amplification rate of the image circuit 14 (i.e. a lower quality display).

One of ordinary skill in the art would recognize, in view of Chee's configuration ability of the individual power saving modes and Takasu's teaching of an user operated power saving button, that the power saving button could also be added to Chee's notebook computer such that a reduced power display results. That is, Chee could similarly has the first power saving mode (PSM 1) triggered by a power saving button resulting in a slow PCLK (i.e. pixel clock) and consequently a slower yet power-saving display.

Therefore, it would have been obvious, for one of ordinary skill in the art, to incorporate Takasu's power saving button into Chee's notebook computer in order to produce a lower power display mode, because of the advantage of allowing an user to manually use a lower power display setting, especially when it's powered by a battery source.

As for claims 45 and 50, it would have been obvious, for one of ordinary skill in the art, to use slower PCLK/pixel clocks (i.e. slower frequency of said drive clock of said display panel) in at least PSM 1 (first power saving mode) and PSM 2 (second power saving mode) whenever a slower display speed is tolerable in exchange for lower power consumption.

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3. Claims 44 and 49 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee and Takasu as applied to claim 43 and 48 above, and further in view of Okumura (US 6,331,844 B1).

As for claims 44 and 49, Chee does not teach controlling drive current of each of the display elements in the first and second power saving modes. Okumura teaches power saving in a type of LCD apparatus, where power saving is attained by controlling the current signal instead of voltage signal (fig. 8; column 13, line 25 through column 14, line 25). Therefore, it would have been obvious to one of ordinary skill in the art to incorporate Okumura's current control setup, when power control using current signal is desired.

4. Claims 46 and 51 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee and Takasu as applied to claim 43 and 48 above, and further in view of Miyamoto (US 6,005,559).

As for claims 46 and 51, Chee does not teach controlling drive voltage of the display panel in the first and second power saving modes. Miyamoto teaches a power conserving display. In particular, Miyamoto's power saving consists of decreasing quantity of backlight, decreasing driving voltage, and increasing the refreshing scanning period (fig. 1; column 3, line 65 to column 4, line 47). It would have been obvious to one of ordinary skill in the art to at least incorporate Miyamoto's control of driving voltage in Chee's notebook computer, as a further compatible option in the control of display power saving.

5. Claims 47 and 52 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee and Takasu as applied to claim 43 and 48 above, and further in view of Ho (US 5,757,365).

As for claims 47 and 52, Chee's teaching of slower pixel clocks in the first power saving mode and the second power saving mode results in lower power consumption, which also reads on claimed modes in which the electric power supplied to said display panel is decrease because broadly speaking a lower power consumption means a lower power supply is required (the claims do not define the way in which power is supplied).

As analyzed above, Chee further teaches entering the third power saving mode when inactivity is determined over a selected time interval (column 6 lines 1-14 and 35-52). However, Chee does not teach claimed third power saving mode in which the image data representing the image to be displayed is modified to save the electric power of said display panel. Ho teaches a computer with a power save controller 110 (fig. 4). Specifically, Ho teaches that upon sensing absence of update data for two full frames (of display), a re-mapping of the grey-scale code to a simple black-and-white two state code occurs. This re-mapped data is used for refresh and results in a power down (column 4 line 63 through column 5 line 17). One of ordinary skill in the art would recognize that Ho's power down mode could similarly be applied to Chee's third power saving mode because it provides a way to lower power consumption in a display only when an image is fixed.

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Therefore, it would have been obvious to one of ordinary skill in the art to incorporate Ho's power down mode into Chee's third power saving mode because of the power saving whenever an image is fixed. This can be done in addition or in place of turning off the LCD backlight as discussed above.

Response to Arguments

6. Applicant's arguments with respect to claims 43-52 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tom V. Sheng whose telephone number is (571) 272-7684. The examiner can normally be reached on 9:00am - 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Tom Sheng
June 28, 2006

AMR A. AWAD
PRIMARY EXAMINER
